

INTEL MIC PROGRAMMING WORKSHOP



IT4Innovations
national
supercomputing
center



DLR Project Management Agency

February 3 - 5, 2016

VŠB – Technical University of Ostrava
IT4Innovations building, room 207

Tutor: Volker Weinberg,
Momme Allalen,
Branislav Jansík

Level:
Basic/Intermediate

Language: English

Tutorial schedule:

February 3, 2016

9:30 – 10:15	Registration, Welcome
10:15 – 11:30	Salomon Intro
11:30 – 13:00	Lunch Break
13:00 – 14:30	Overview of the Intel MIC
14:30 – 15:00	Coffee Break
15:00 – 16:30	OpenMP and Offloading, Part 1
16:30 – 17:00	Coffee Break
17:00 – 18:00	OpenMP and Offloading, Part 2

February 4, 2016

09:00 – 10:30	MPI
10:30 – 11:00	Coffee Break
11:00 – 12:45	MKL
12:45 – 14:00	Lunch Break
14:00 – 15:30	Vectorisation and Basic Xeon Phi Performance Optimisation

February 5, 2016

SeisMIC - Seismic Simulation
on Current and Future Supercomputers



More information and registration at:
<http://prace.it4i.cz/en/SMIC-02-2016>

Deadline for registration:
January 27, 2016

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training@it4i.cz

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